

Electrical Conductivity and Hall Effect Measurements of (CuInTe₂) Thin Films

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Abstract

In this research, the electrical conductivity and Hall effect measurements have been investigated on the CuInTe₂ (CIT) thin films prepared by thermal evaporation technique on glass substrate at room temperature as a function of annealing temperature (R.T,473,673)K for different thicknesses (300 and 600) nm. The samples were annealed for one hour.

The electrical conductivity analysis results demonstrated that all samples prepared have two types of transport mechanisms of free carriers with two values of activation energy (E_{a1} , E_{a2}), and the electrical conductivity increases with the increase of annealing temperature whereas it showed opposite trend with thickness, where the electrical conductivity would decrease as the film thickness increases.

The results of Hall effect measurements of CuInTe₂ films show that all films were (p-type), the carrier concentration and Hall mobility are strongly dependent on the annealing temperature and film thickness.

Key words: CuInTe₂, Electrical conductivity, Hall Effect, Thermal Evaporation.

Introduction

The ternary compound I-III-VI₂ chalcopyrite semiconductors thin films (I=Cu,Ag, III=Al,Ga,In, and VI=S,Se,Te) are feasible candidates in recent years due to their potential for application in photovoltaic solar cells and variety of opto-electronic devices such as infrared detectors, light emitting diodes, up converters, optical parametric oscillators and IR generators also in optical frequency conversion applications in solid state based tunable laser systems [1-7].

CuInTe₂ (CIT) crystallizes in the tetragonal chalcopyrite structure [5] has a direct band gap varying between 0.92 eV and 1.04 eV at approximately 300 K and high optical absorption coefficient (10⁵ cm⁻¹) which falls in the optimum range to make use of bottom cells for multi-junction (tandem) solar cells [1-7]. This minimizes the requirement for long minority carrier diffusion lengths and needs only a few tenth to a few micron of thickness to make devices, thus minimizing the cost of material [5,8]. Thin films of CIT can be obtained n or p conducting depending on the preparation conditions.[9] Much work on CuInTe₂ was done in order to get better understanding of its optical, electronic and electrical properties using several methods of deposition techniques such as electrodeposition [5,10], bridge man technique [2,11], three-source co-evaporation technique [4,7,12], flash evaporation [13], pulsed laser deposition [14], thermal vacuum evaporation [15], etc. In the present work, CuInTe₂ films was prepared by thermal evaporation technique, the aim of this research is to collect more information about the electrical properties of these films through the electrical conductivity, and Hall effect measurements.

Experiment

Film Preparation

CuInTe₂ (CIT) films were prepared by the alloy which was obtained by mixing of the appropriate quantities of high purity (99.999%) material of copper, indium and tellurium in evacuated fused quartz ampoules, heated at (1473 K) for 12h. The ampoules quenched rapidly in cold water.

CuInTe₂(CIT) films were grown onto a glass slide substrate kept at R.T by thermal evaporation technique in a high vacuum system, the base pressure during the evaporation was (3x10⁻⁶) Torr using Edward coating unit model (E 306) from molybdenum boat with thickness (300,600) nm. The distance from molybdenum boat to sample holder was about (18 cm), Al electrodes were used as contact material for making the electrical connections. After deposition the samples were annealed at (473, 673) K for one hour.

Characterization of CuInTe₂ Thin Films

For D.C. measurement the variation of electric resistance (R) with temperature range (293- 473) K, were measured using Keithly model 616, then calculated the resistivity (ρ) by equation [16]:-

$$\rho = \frac{R \cdot b \cdot t}{L} \dots\dots\dots (1)$$

Where t is film thickness, b is electrodes width; L is distance between two Al electrodes. The resistivity is related to the conductivity (σ) by the formula [16]:-

$$\sigma_{d.c} = \frac{1}{\rho} \dots\dots\dots (2)$$

Hall effect measurements have been carried out to investigate the type of charge carriers, carrier concentrations (n_H) and Hall mobility (μ_H) using the Ecopia HMS-3000 Hall measurement system. The sign of the Hall coefficient (R_H) of semiconductor is determined by

the sign of the charge carriers. If the conduction is due to one carrier type , we can measure the carrier concentration according to the relation: [17,18]

$$n_H = \pm 1 / R_H . e \dots\dots\dots (3)$$

The mobility is related to the Hall coefficient by equation: [18]

$$\mu_H = \sigma / n_H . e = \sigma . | R_H | \dots\dots\dots (4)$$

Where σ is the conductivity.

The films thickness were measured by using the weighing method according to the following relation[19]:

$$t = m / A . \rho \dots\dots\dots (5)$$

Where: t = film thickness, m = mass of film, ρ = density of film, A = films area. Using a sensitive balance whose sensitivity of the order (10^{-4}).

Results and Discussion

Figure (1) shows the variation in the resistivity of CuInTe₂ (CIT) films as a function of annealing temperature (R.T,473,673)K for different thicknesses (300 and 600)nm, we can deduct from this Figure that the resistivity values decrease as the annealing temperature increases due to the improvement in the films structure that lead to reduce dangling bonds, defects like vacancy sites, trapping centers of charge carriers and point defect cluster in the films structure[15], this is, perhaps, because of the decreased grain boundary scattering, therefore the number of carriers available for transport increases with the improvement in the electrical conductivity and decreases the resistivity of the films. In addition to that the resistivity values are in good agreement with references [7 and14].

Figure (2) shows a relationship of $\ln\sigma$ of of CuInTe₂ (CIT) films versus $10^3/T$ in the temperature range (293- 473) K as a function of annealing temperature (R.T,473,673)K for different thicknesses (300 and 600)nm, it is clear from this figure the general behavior of the films is similar to other semiconductors and the electrical conductivity increases as the annealing temperature increases because of the increase number of carriers available for transport for the same reasons as we mentioned before see Table 1. This figure appears to separate two temperature ranges characterized by different conductivity slopes which means that all (CIT) films have two mechanisms for electrical conductivity and there is two mechanisms of transport of free carriers with two values of activation energy (E_{a1} , E_{a2}). At higher temperature range (403-473) K, the conduction mechanism is due to carriers excited into extended states beyond the mobility edge and the small values of activation energies at lower temperature range (293- 393) K indicated carriers excited into the localized states at the edge of the band and hopping , such observations were also seen by references.[2,4,2 and21] . Figure (3) and Table 1, show the activation energies varies with increase both the annealing temperature and thickness for CIT films and this may be due to change in crystal structure with these parameter. The values of activation energies are in agreement with those reported by other workers[4].

All CIT films exhibit p-type conductivity, the Hall coefficients for all prepared films are positive, which means that the holes are majority charge carriers in the conduction process and the type of conduction was p-type, this result is in agreement with references [2,4,14,20and22]. The influence of annealing temperature (R.T,473,673)K for different thicknesses (300 and 600)nm on the carrier concentration and Hall mobility are shown in Table 2 and figures (4)and(5) respectively. It can be seen from figure (4) and Table (2) that carrier concentration (n_H) increases some order of magnitude as the annealing temperature increases for different thicknesses, this may be due to the increase of grain size or decreasing of grain boundary scattering and this is because of the improved film structure which increases the number of charge carriers because of the reducing of grain boundary barrier height. Also it shows the carrier concentration values decrease with the increase of film thickness may be due to the increase of the energy gap. In addition to that the carrier

concentration and carrier mobility's values are in contrast with the result obtained by references.[2and20]

Conclusion

In the present work, the effect of annealing temperature and thickness on the electrical properties of CuInTe₂ (CIT) films prepared by thermal evaporation method are studied in detail, through measurements of conductivity and Hall effect. Throughout our research we showed that the thermal evaporation was a good method to prepare (CIT) film at R.T from alloy, the electrical conductivity and activation energies are strongly dependent on the annealing temperature and film thickness and the CIT films contain two types of transport mechanisms.

We should mention that the resistivity of these films is small; therefore these samples can be used as an absorber layer in the fabrication of solar cell. Hall effect measurements demonstrate that the CIT films were p-type, both the mobility and concentration of the charge carriers are seen to be dependent on the annealing temperature and film thickness.

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Table (1): The electrical conductivity and activation energies of CIT films at different annealing temperatures and thicknesses.

Films thickness (nm)	Ta (K)	$\sigma_{R.T}$ ($\Omega.cm$) ⁻¹	Ea ₁	Tem. range	Ea ₂	Tem. range
			(eV)	(K)	(eV)	(K)
300	R.T	19.9005	0.08137	293-393	0.1265	403-473
	473	78.125	0.04827	293-393	0.08935	403-473
	673	278.086	0.03300	293-393	0.03515	403-473
600	R.T	6.313	0.05281	293-383	0.16445	393-473
	473	7.731	0.06911	293-383	0.12018	393-473
	673	41.66	0.06991	293-383	0.02514	393-473

Table (2) Values of Carrier Concentration and Carrier Mobility of CIT films at different annealing temperatures and thicknesses.

Thickness (nm)	Ta (K)	Carrier Concentration n _H (cm ⁻³)	Carrier Mobility μ_H (cm ² /v. s.)
300	R.T	5.13 E+15	1.70 E+03
	473	5.45 E+18	4.48E+01
	673	8.28 E+19	1.50E+00
600	R.T	1.55E+12	2.21E+01
	473	3.97E+12	6.23E+00
	673	2.16E+15	1.37E+02

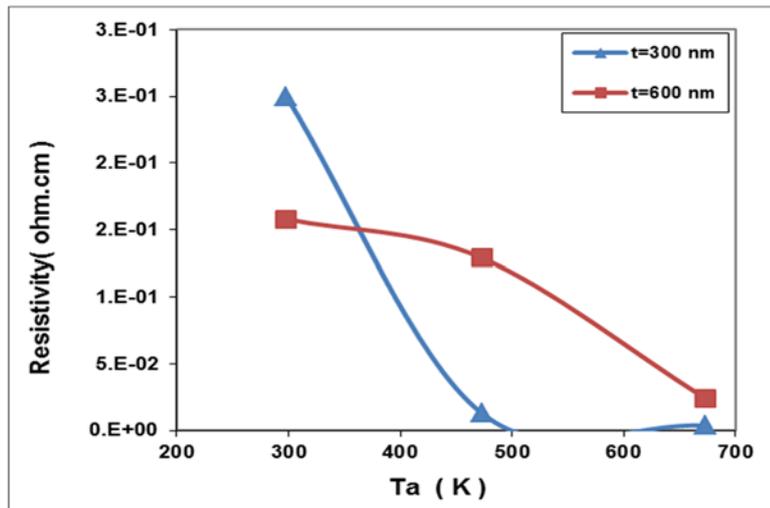
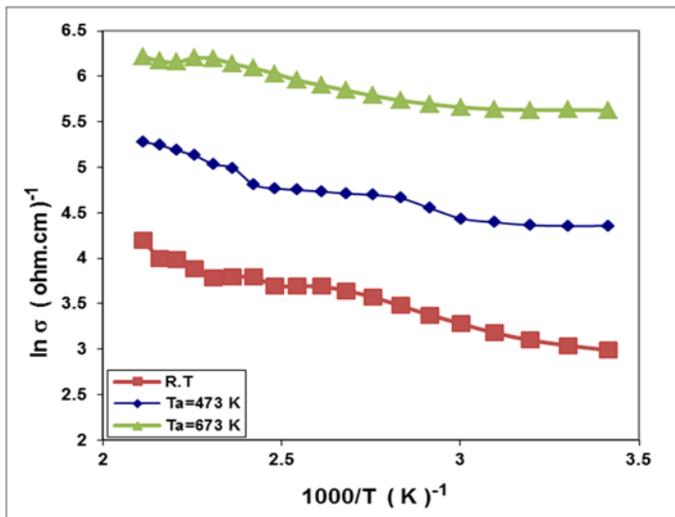
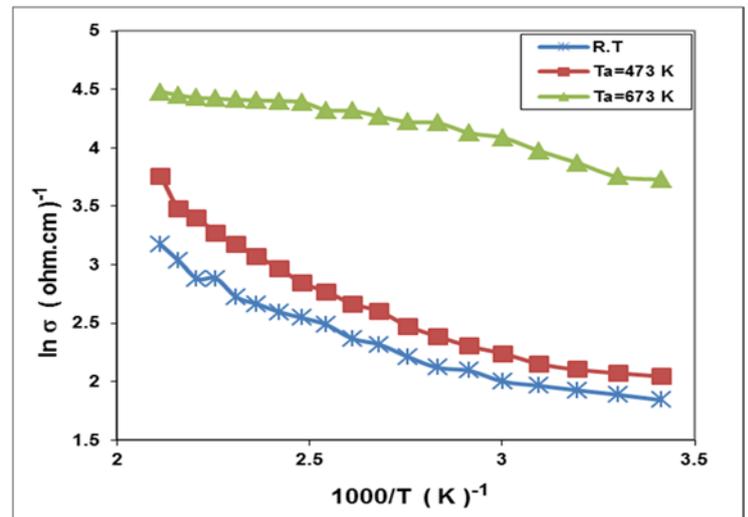


Figure (1): Variation resistivity of CIT films as a function of annealing temperature of different thicknesses.



(a)



(b)

Figure (2): Variation $\ln \sigma$ versus $10^3/T$ of CIT films as a function of annealing temperature of different thicknesses. (a) 300nm (b)600 nm

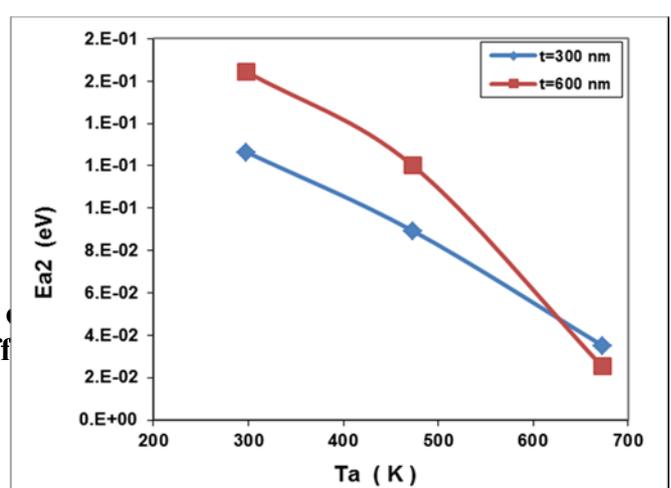
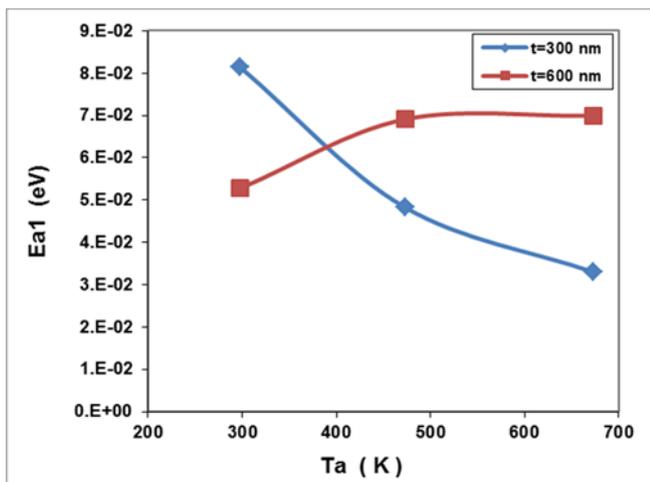


Figure (3): Variation activation energies of (CIT) films as a function of annealing temperature of different thicknesses.

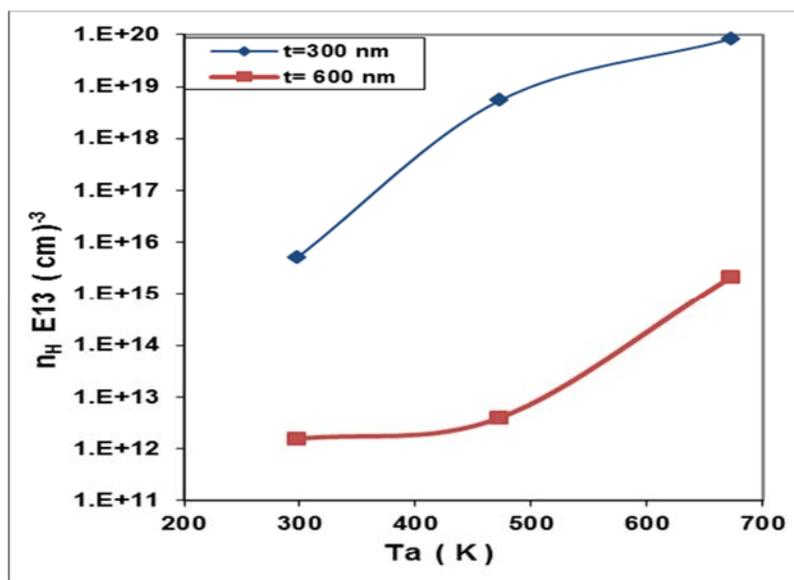


Figure (4): Variation of the charge Carrier's concentration of (CIT) films as a function of annealing temperature of different thicknesses.

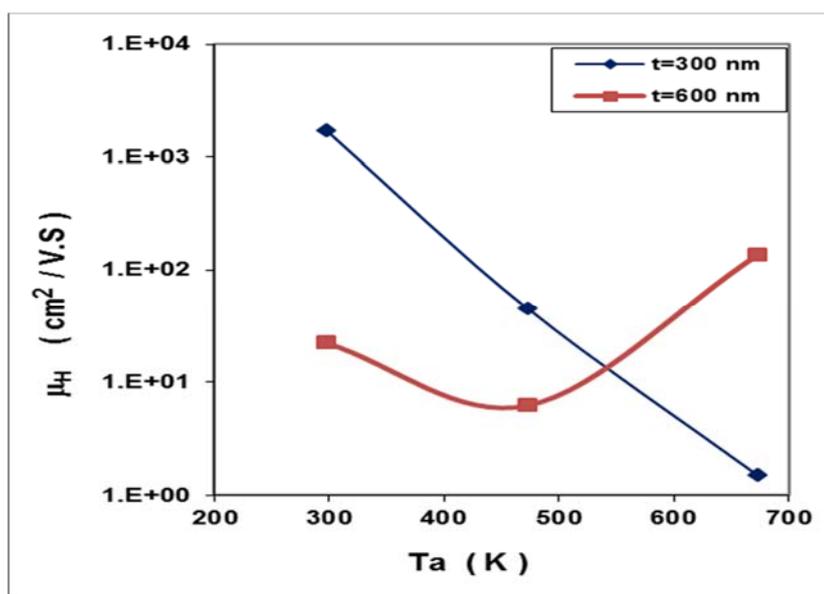


Figure (5): Variation Hall mobility of (CIT) films as a function of annealing temperature of different thicknesses.

التوصيلية الكهربائية وقياسات تأثير هول لأغشية (CuInTe₂) الرقيقة

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الخلاصة

تم في هذا البحث حساب التوصيلية الكهربائية وقياسات تأثير هول لأغشية (CuInTe₂) الرقيقة والمحضرة باستعمال تقنية التبخير الحراري على ارضيات من الزجاج عند درجة حرارة الغرفة كدالة لدرجة حرارة التلدين (R.T, 473, 673)K ولسمك مختلف (300, 600)nm ولدنت النماذج لمدة ساعة واحدة. اوضحت نتائج تحليل التوصيلية الكهربائية ان جميع الاغشية المحضرة تمتلك آليتين للانتقال الالكتروني لحاملات الشحنة وقيمتين لطاقت التنشيط (Ea₁, Ea₂) ولوحظ زيادة التوصيلية الكهربائية مع زيادة درجة حرارة التلدين وظهرت سلوكا معاكسا مع السمك اذ تناقصت قيم التوصيلية الكهربائية مع زيادة سمك الغشاء. وبينت نتائج قياسات تأثير هول ان جميع أغشية CuInTe₂ كانت من نوع (p-type) . واعتمدت قيم كل من تركيز حاملات الشحنة وحركية هول على درجة حرارة التلدين وسمك الاغشية.

الكلمات المفتاحية: - CuInTe₂ ، التوصيلية الكهربائية ، تأثير هول ، التبخير الحراري.